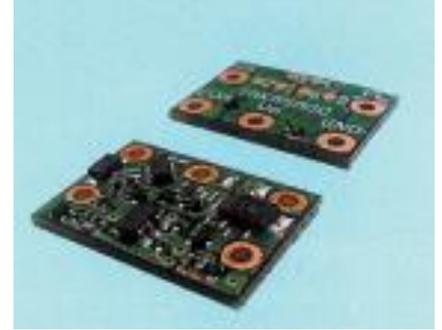


For operating solenoid valves efficiently

“Hit and Hold” Circuit

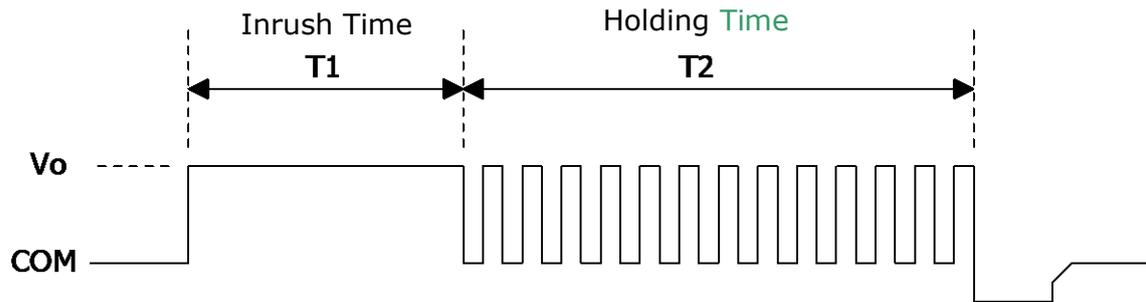
Once switched to ON-position by energising at the rated voltage, a solenoid valve can hold the ON-position status even after the applied voltage is dropped to a lower voltage, which is called a holding voltage. Using this characteristic, various benefits can be achieved.

- ① Lower power consumption
- ② Reduction of coil heat-generation
- ③ Improvement of response time
- ④ Increase of operating pressure
- ⑤ Minimisation of size
- ⑥ Etc.



A “Hit and Hold” circuit automatically drops the applied voltage to a lower value, which is a holding voltage, after a very short period of time. Models where the circuit is built-in are available. Please contact us for the use to meet your needs.

Timing Diagram

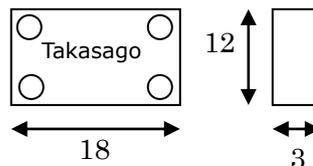


Voltage reduction is substantially achieved by averaging the voltage value with PWM (Pulse Width Modulation) of the “Hit & Hold” circuit during the holding time (T2), which starts after the inrush time (T1) and ends with ceasing supply of the current, upon supply of the applied voltage (Vo).

Specifications

Input	5VDC - 27VDC
Inrush Time	100ms, 300 ms
Output	40% of Input (after Inrush Time)

Dimensions (mm)



Note: Specifications etc. may be changed at any time without notice.

TAKASAGO ELECTRIC, INC.

66 KAKITSUBATA, NARUMI-CHO, MIDORI-KU, NAGOYA, 458-8522 JAPAN

Tel +81-52-891-2301 Fax+81-52-891-7386

E-mail: info@takasago-elec.co.jp